8\6\2020  
I have been reading from the operating systems text so the development on my computer has been slow. I might work on this for a bit longer before I resume the text.   
  
  
  
  
  
  
x\x\2020  
I am encountering some uncertainties regarding the design of the rest of my computer while working on other modules like the interrupt module. So I think I should focus on getting the foundation of the computer designed simultaneously while I build other conceptual modules.

In particular, I have no idea what the data bus should look like when it is finished, and the interrupt module’s address generator must be connected to a specific data-line to interface with the instruction register.  
  
  
Maybe after designing a specific module I can add it to a larger more comprehensive design, just to see how every other module works together.